

APPARATUS AND METHOD FOR TRACE
STREAM IDENTIFICATION OF MULTIPLE
TARGET PROCESSOR EVENTS

Abstract of the Invention

1 When a plurality of simultaneous, preselected target
2 processor events are detected, a multiple-event sync marker
3 is generated that identifies the preselected events and
4 relates the occurrence of these events to timing trace
5 stream. The sync marker for the plurality of preselected
6 events differs from a single event sync marker by including
7 at least one additional packet. The additional packet
8 includes logic signals stored at locations related to each
9 identified event.